



PDPM Indian Institute of Information Technology, Design and Manufacturing Jabalpur
Dumna Airport road, Po: Khamaria, Jabalpur, Madhya Pradesh-482005

ADVERTISEMENT

Applications are invited from the Indian nationals for the position of **Junior Research Fellow (JRF)** to work in the Interdisciplinary **Cyber Physical Systems (ICPS)** - DST funded research project entitled “**FPGA prototype of non-recursive key based cryptosystem for secure transmission of real time privacy signal**”. Selected candidate will also be encouraged for the registration in Ph.D. program as per Institute rules.

Post: **Junior Research Fellow (JRF)**

Number of Posts: **1**

Project Duration: 1 years or till the completion of the project, whichever is earlier (the position is purely temporary in nature and performance will be reviewed periodically) extendable on approval of ICPS, DST as per ICPS guidelines.

Stipend: **₹ 25,000 per month and HRA as admissible depending upon merits, suitability, qualifications and as per the ICPS, DST guidelines.**

Essential Qualifications:

BE/B.Tech/ME/M.Tech in Electronics and Communication / VLSI / Computer Science / Information Technology or any other related subject with minimum CPI of 5.5 or 55% marks aggregate in the last degree.

Candidates having good academic and research background with GATE Qualification.

Desirables: Candidates having knowledge of Communication/ FPGA (VHDL/Verilog) / Xilinx / Cryptography will be preferred.

Selection Procedure: Interview will be conducted in online mode (preferably ZOOM). The shortlisted candidate would be notified through email about the ZOOM Meeting Details.

How to Apply?

A detailed *Biodata* may be sent by e-mail to varunb@iiitdmj.ac.in and the hard copy to be brought on the date of interview. Please note that no TA/DA will be given to the candidates called for interview. Incomplete application or only CV shall not be entertained.

Last date for receipt of applications by email: **Feb 20, 2021**. Please note that the list of shortlisted candidates and date of interview shall be notified on the web portal after **Feb 20, 2021**.

Contact: Principal Investigator (PI)

Dr. Varun Bajaj

Department of Electronics and Communication

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